

*SPECIFICATION AMENDMENTS*

Replace the paragraph beginning at page 7, line 3 with:

Fig. 1 shows an input side matching circuit in a first embodiment of the present invention. In Fig. 1, reference numeral 5 denotes an input terminal. Reference numerals 10 and 12 denote lines. Reference numeral 26 denotes a transistor. Reference numeral 30 denotes a capacitor. Reference character L1 denotes an open stub ~~capacity~~ capacitance. Reference character L2 denotes an inductance of the line 12. Reference character CA denotes ~~an capacitance of the MIM capacity of the~~ capacitance of the MIM capacity of the capacitor 30. Reference character a denotes a point ~~for denoting a of the gate-source capacity capacitance~~ for denoting a of the gate-source capacity capacitance when viewing it from the input side of the transistor 26 and reference character b denotes a point ~~when the point is viewed~~ for viewing the gate-source capacitance together with the MIM capacity capacitance CA. Reference character c denotes a point ~~when viewing it is viewed~~ for viewing the gate-source capacitance together with the inductance L2 and reference character d denotes a point ~~when it is viewed~~ for viewing the gate-source capacitance together with the open stub capacity capacitance L1 from the input terminal 5. There is only ~~a~~ one difference between the input side matching circuit shown in Fig. 1 and the conventional input side matching circuit shown in Fig. 10(A). The difference is ~~that an the MIM capacity capacitance CA is connected to the~~ portion point A in Fig. 10 1.